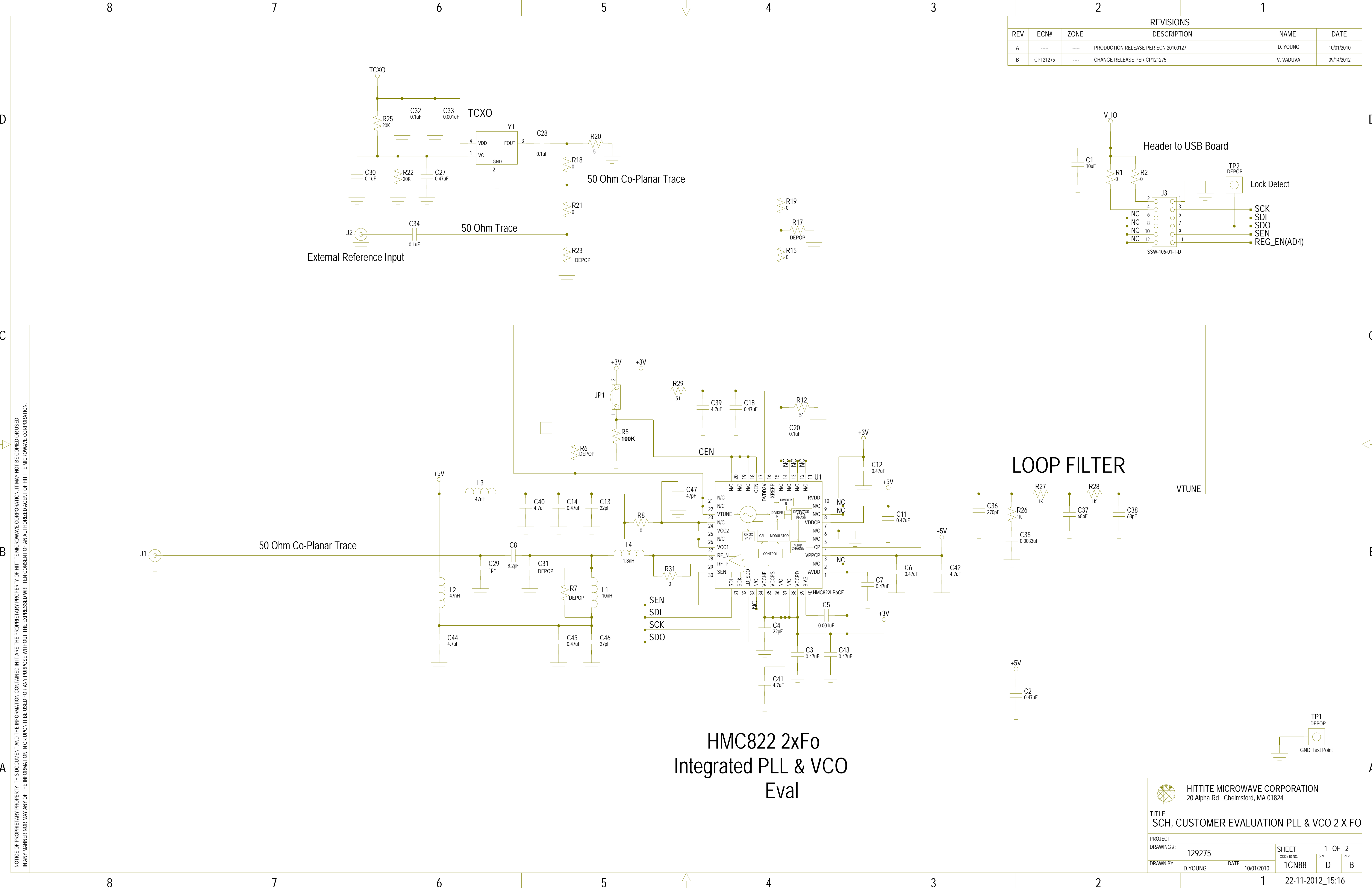



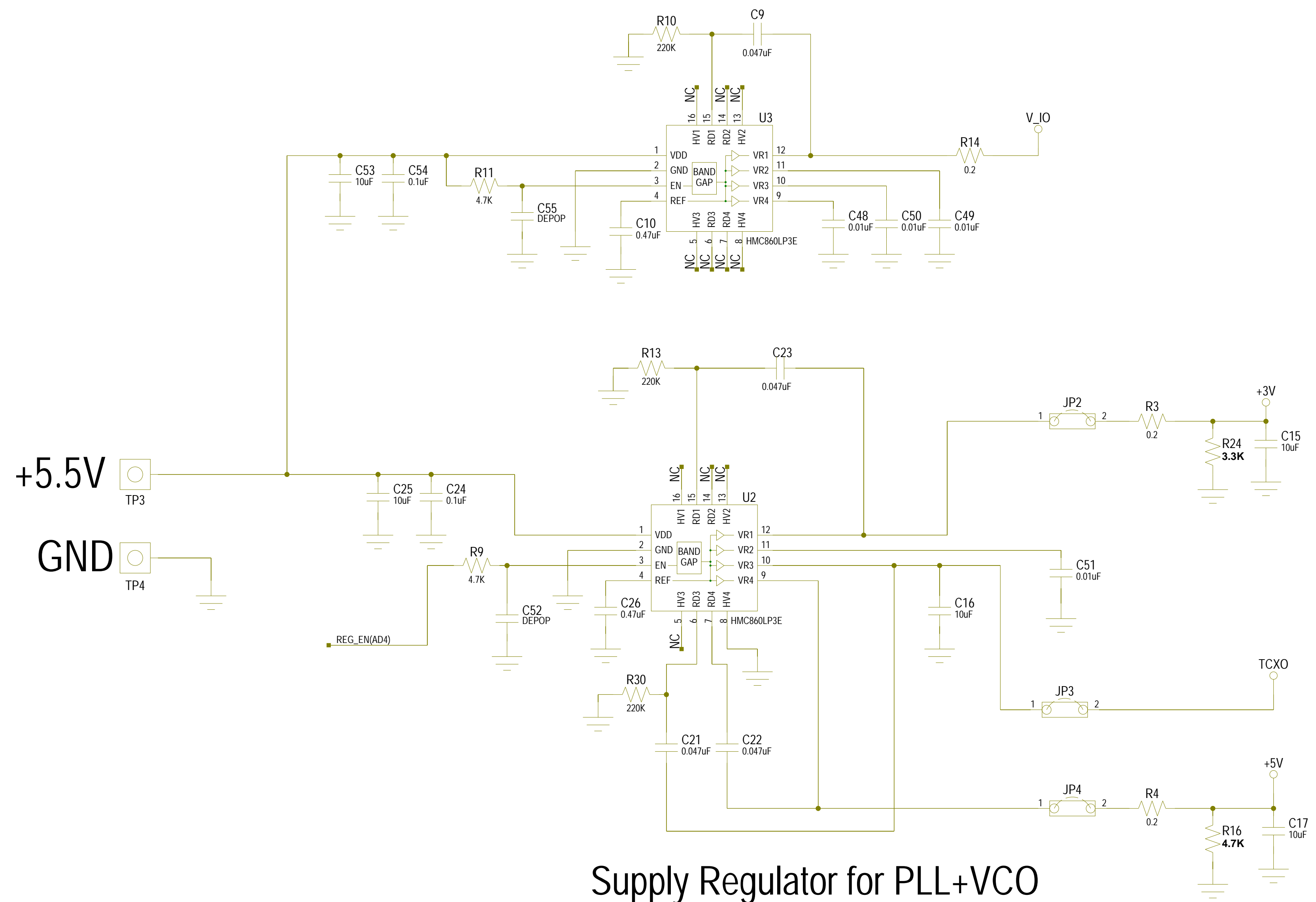
A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control
Small series resistors required in VR1 and VR4 output paths, as shown



REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
A	PRODUCTION RELEASE PER ECN 20100127	D. YOUNG	10/01/2010
B	CP121275	CHANGE RELEASE PER CP121275	V. VADUVA	09/14/2012

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 HITTITE MICROWAVE CORPORATION 20 Alpha Rd Chelmsford, MA 01824					
TITLE SCH, CUSTOMER EVALUATION PLL & VCO 2 X FO					
PROJECT			SHEET 1 OF 2		
DRAWING #: 129275			CODE ID NO.	SIZE	REV
DRAWN BY: D.YOUNG			DATE: 10/01/2010	1CN88	D B



Supply Regulator for PLL+VCO

A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control
Small series resistors required in VR1 and VR4 output paths, as shown